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Sheet 1 of 1

Form PTO-1449		Docket Number 751	Application Number 08/464,034
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		Applicant Gilbert Hyatt	
		Filing Date June 5, 1995	Group Art Unit

U.S. PATENT DOCUMENTS					
EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
	3,579,201	05/71	Langley	712	245
	3,657,706	04/72	Horgan et al	345	2.1
	3,675,219	07/72	Kawasaki et al	365	73
	4,559,555	12/85	Schoolman		
	4,669,812	06/87	Hoebing	359	9
	4,674,046	06/87	Ozeki et al	382	131
	4,737,921	04/88	Goldwasser et al	345	421
	5,093,567	03/92	Staveley	250	221
	5,130,794	07/92	Ritchey	348	39
	5,135,397	08/92	Yen	434	2
	5,255,211	10/93	Redmond	703	6

OTHER DOCUMENTS <small>(including Author, Title, Date, Pertinent Pages, Etc.)</small>	
Ferrite Memory systems by Russel, Louis A. et al., dated IEEE Transactions on Magnetics vol. mag-4, no. 2 June 1968, pp. 134-145.	
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Designing a System on a Chip by Spitalny, Arnold, annual ACM IEEE Design Automation Conference Proceeding of the June 1970 design automation workshop of Design automation, pp. 336-345	

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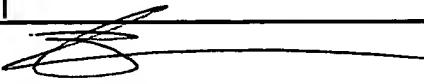
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EXAMINER	BRIAN WERNER <i>[Signature]</i> PRIMARY EXAMINER	DATE CONSIDERED	<i>4/26/04</i>
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